

## Product Summary

$V_{RRM}$	650 V
$I_F (T_c=155^\circ\text{C})$	8 A
$Q_c$	24 nC

## Features

- Extremely low reverse current
- No reverse recovery current
- Temperature independent switching
- Positive temperature coefficient on  $V_F$
- Excellent surge current capability
- Low capacitive charge

## Benefits

- Essentially no switching losses
- System efficiency improvement over Si diodes
- Increased power density
- Enabling higher switching frequency
- Reduction of heat sink requirements
- System cost savings due to smaller magnetics
- Reduced EMI

## Applications

- Switch mode power supplies (SMPS)
- Uninterruptible power supplies
- Motor drivers
- Power factor correction

## Package Pin Definitions

- Pin1 and backside - Cathode
- Pin2 - Anode

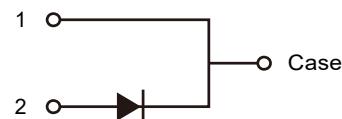
## Package Parameters

Part Number	Marking	Package
B1D08065K	B1D08065K	TO-220-2

## Package: TO-220-2



## Electrical Connection



**Maximum Ratings ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

Symbol	Parameter	Test conditions	Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage		650	V
$V_{RSM}$	Non-repetitive peak reverse voltage		650	V
$I_F$	Continuous forward current	$T_c=25^\circ\text{C}$ $T_c=155^\circ\text{C}$	29 8	A
$I_{FSM}$	Non-repetitive forward surge current	$T_c=25^\circ\text{C}, t_p=10\text{ms}$ Half sine wave	60	A
$\int i^2 dt$	i <sup>2</sup> t value	$T_c=25^\circ\text{C}, t_p=10\text{ms}$	18	A <sup>2</sup> S
$P_{tot}$	Power dissipation	$T_c=25^\circ\text{C}$ $T_c=110^\circ\text{C}$	129 56	W
$T_j$	Operating junction temperature		-55~175	°C
$T_{stg}$	Storage temperature		-55~175	°C
	TO-220 mounting torque	M3 Screw	0.7	Nm

**Thermal Characteristics**

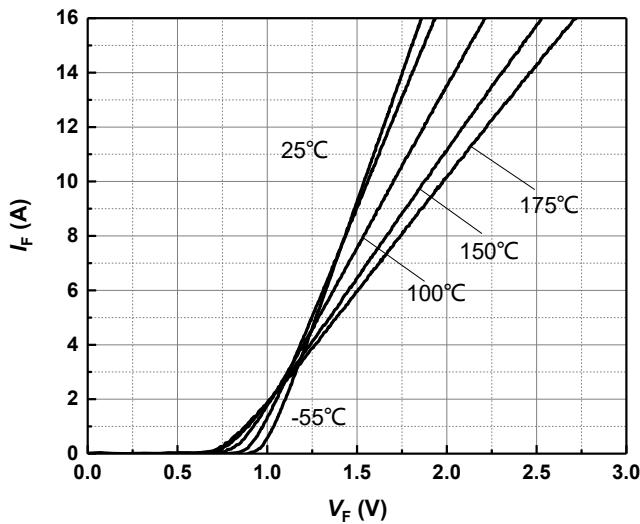
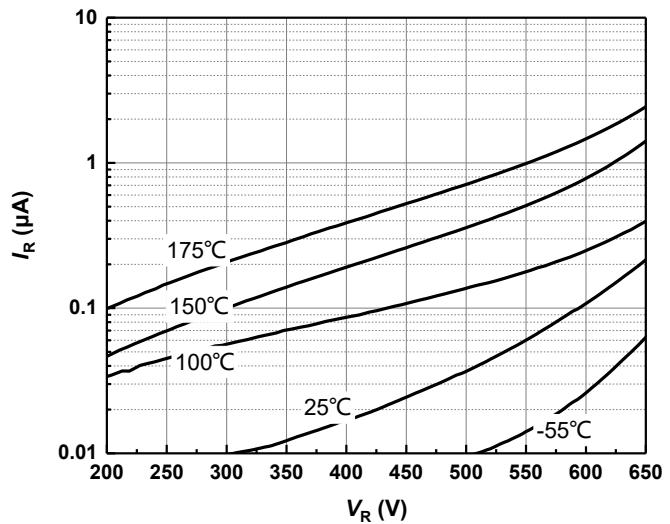
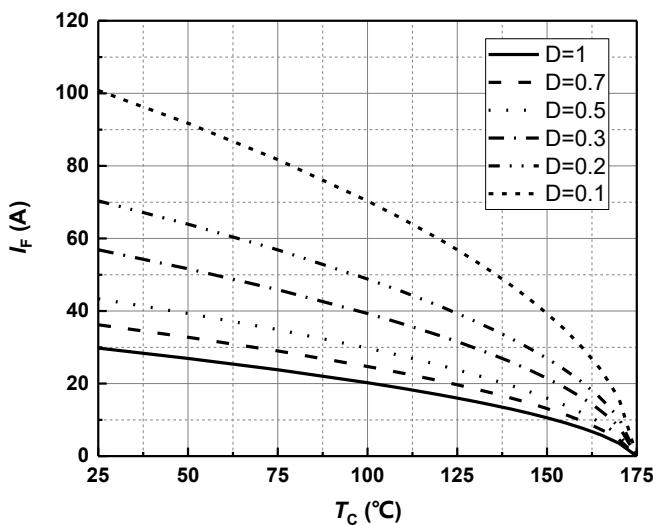
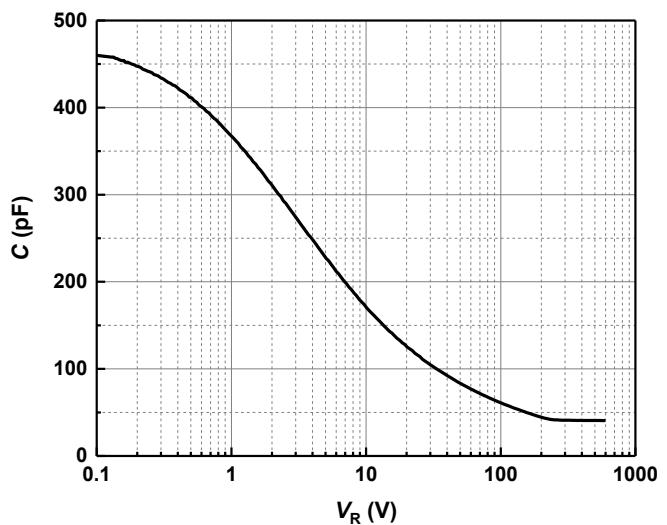
Symbol	Parameter	Value			Unit
		Min.	Typ.	Max.	
$R_{th(jc)}$	Thermal resistance from junction to case		1.154		K/W

**Electrical Characteristics**
**Static Characteristics**

<b>Symbol</b>	<b>Parameter</b>	<b>Test conditions</b>	<b>Value</b>			<b>Unit</b>
			<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	
$V_{DC}$	DC blocking voltage	$T_j=25^\circ C$	650			V
$V_F$	Diode forward voltage	$I_F=8A T_j=25^\circ C$ $I_F=8A T_j=175^\circ C$		1.44 1.75		V
$I_R$	Reverse current	$V_R=650V T_j=25^\circ C$ $V_R=650V T_j=175^\circ C$		1 10		$\mu A$

**AC Characteristics**

<b>Symbol</b>	<b>Parameter</b>	<b>Test conditions</b>	<b>Value</b>			<b>Unit</b>
			<b>Min.</b>	<b>Typ.</b>	<b>Max.</b>	
$Q_C$	Total capacitive charge	$V_R=400V T_j=25^\circ C$ $Q_C=\int_0^{V_R} C(V)dV$		24		nC
C	Total capacitance	$V_R=1V f=1MHz$ $V_R=300V f=1MHz$ $V_R=600V f=1MHz$		365 41.1 40.7		pF
$E_C$	Capacitance stored energy	$V_R=400V$		6		$\mu J$

**Typical Performance**

**Figure 1** Typical forward characteristics

**Figure 2** Typical reverse current as function of reverse voltage

**Figure 3** Diode forward current as function of temperature, D=duty cycle

**Figure 4** Typical capacitance as function of reverse voltage,  $C=f(V_R)$ ;  $T_j=25^\circ\text{C}$ ;  $f=1 \text{ MHz}$

### Typical Performance

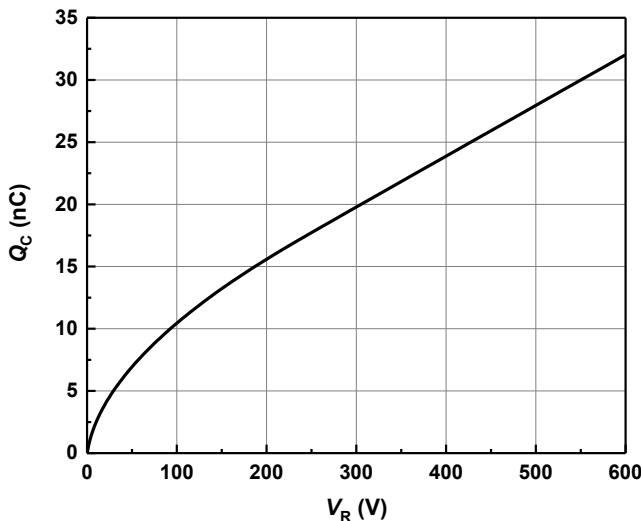


Figure 5    Typical reverse charge as function of reverse voltage

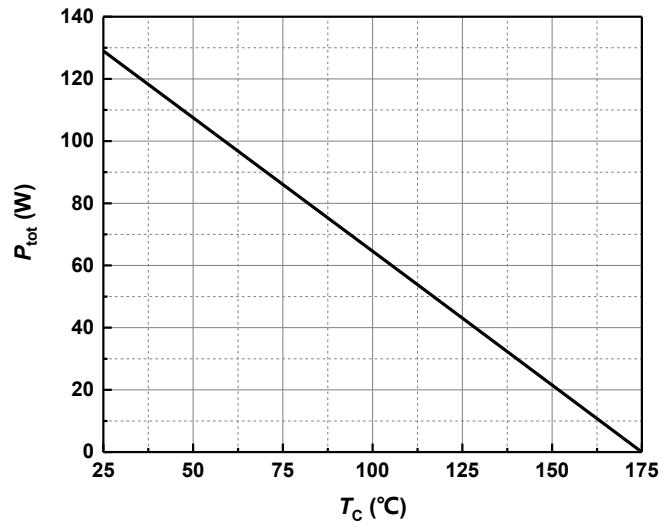


Figure 6    Power dissipation as function of case temperature

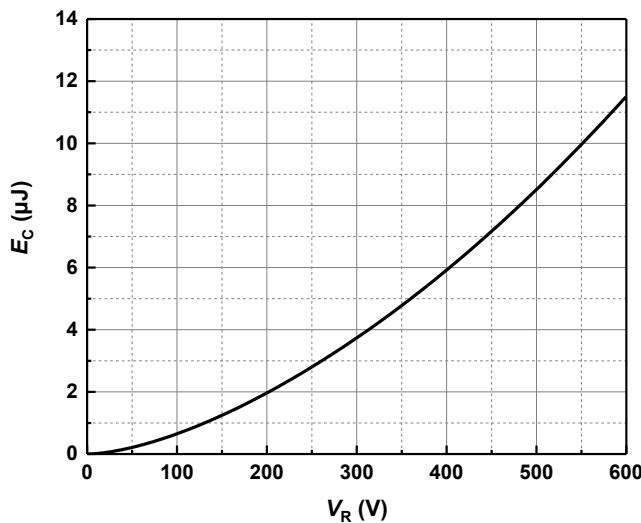


Figure 7    Capacitance stored energy

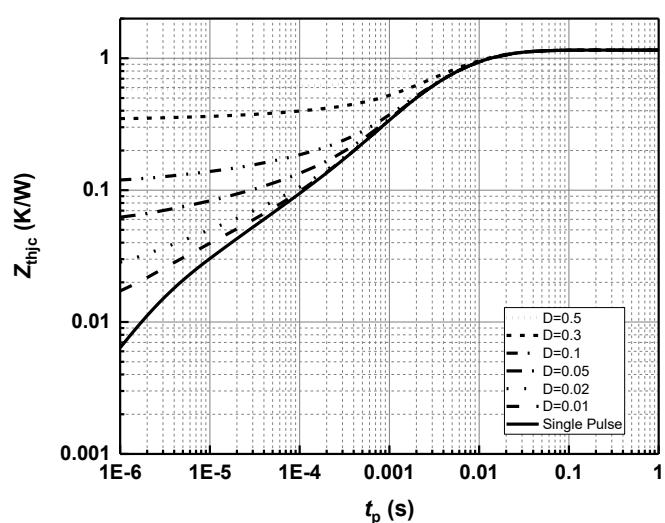
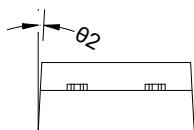
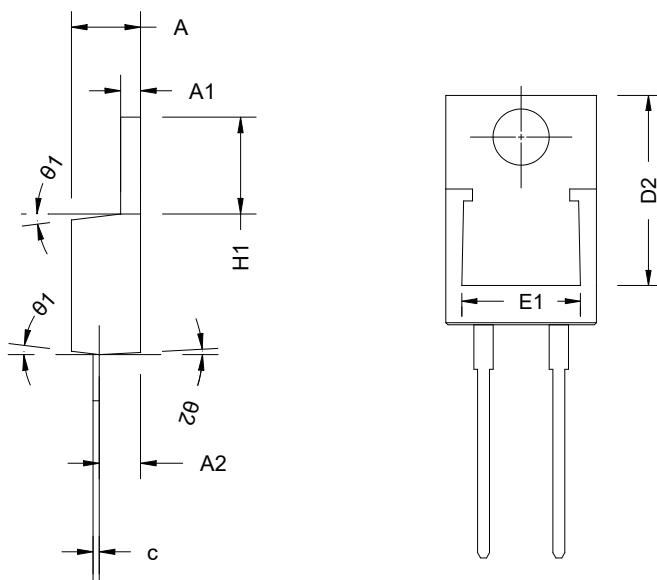
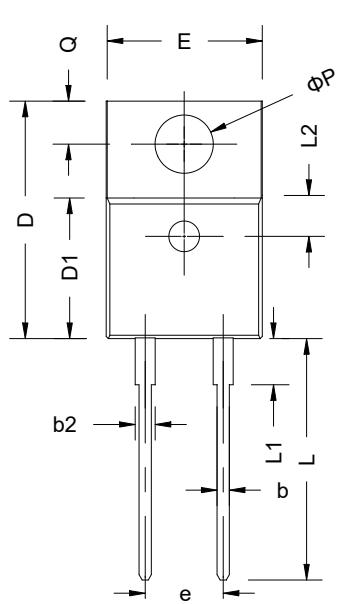


Figure 8    Max. transient thermal impedance,  $Z_{thjc} = f(t_p)$ , parameter:  $D = t_p / T$

**Package Dimensions**


SYMBOL	mm		
	MIN	NOM	MAX
A	4.37	4.57	4.77
A1	1.22	-	1.40
A2	2.49	2.69	2.89
b	0.75	-	0.96
b2	1.22	-	1.47
c	0.30	-	0.48
D	15.15	15.45	15.75
D1	9.05	9.15	9.25
D2	11.40	-	12.88
E	9.86	10.16	10.36
E1	6.86	-	8.89
e	4.98	5.08	5.18
H1	6.10	6.30	6.50
L	12.70	-	13.70
L1	-	-	4.10
L2	2.50 REF		
$\phi_P$	3.70	3.84	3.99
Q	2.54	-	2.94
$\theta_1$	5°	7°	9°
$\theta_2$	1°	3°	5°

## Revision History

Document Version	Date of Release	Description of Changes
Rev. 2.0	2020-07-06	Characteristics updated.
Rev. 2.1	2020-11-08	Characteristics updated.
Rev. 2.2	2021-08-24	POD updated.
Rev. 2.3	2021-12-04	Updated.

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